

M. De Pascale 15.12.2023 | HPCQC 2023

Upscaling QC Simulators on HPC Systems Where I'll take you



Introduce LRZ and QC Team

Software Portfolio

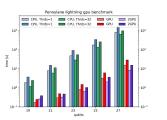
Deployments

• Deployment results









## Leibniz Supercomputing Center

- Est. 1967
- Supercomputing centre from State of Bavaria
- Provides services to public research institutes (e.g. Munich Universities)
- SuperMUC-NG (40<sup>th</sup> in top 500 11.2023) - Lenovo
  - 311,000 compute cores
  - Peak perf 26.9 PFlops
  - Intel Xeon Skylake
- Al Cluster
  - 4 NVIDIA DGX A100, 252 CPU cores/node, 80GB/GPU
  - 1 NVIDIA DGX V100, 76 CPU cores/node, 16GB/GPU





The Quantum Computing and Technologies Department (QCT)

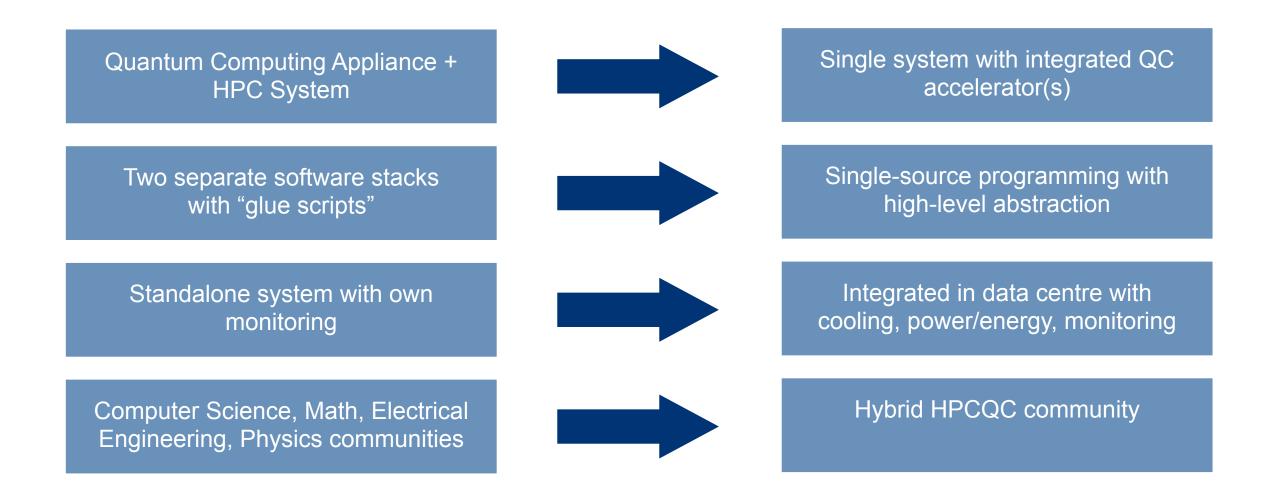
- Established in April 2022
- 30+ people of many nationalities (Africa, Asia, Middle East, Europe, US)
- Four Groups:
  - Program Development
  - Quantum system operations
  - Development of the integrated HPCQC software stack
  - Establishing services in QC on HPC infrastructure





## Upscaling QC Simulators on HPC Systems HPCQC End goals











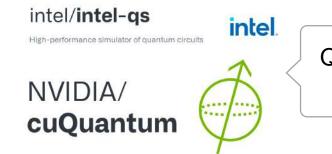
Dedicated Quantum Simulator

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Dedicated Quantum Simulator



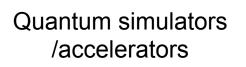
Quantum simulators /accelerators





Dedicated Quantum Simulator

intel/ <b>intel-qs</b>	intel
NVIDIA/ cuQuantum	Ď



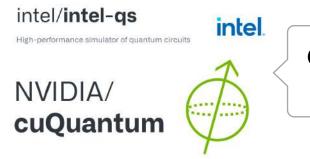


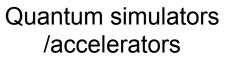
Software Development Frameworks





Dedicated Quantum Simulator











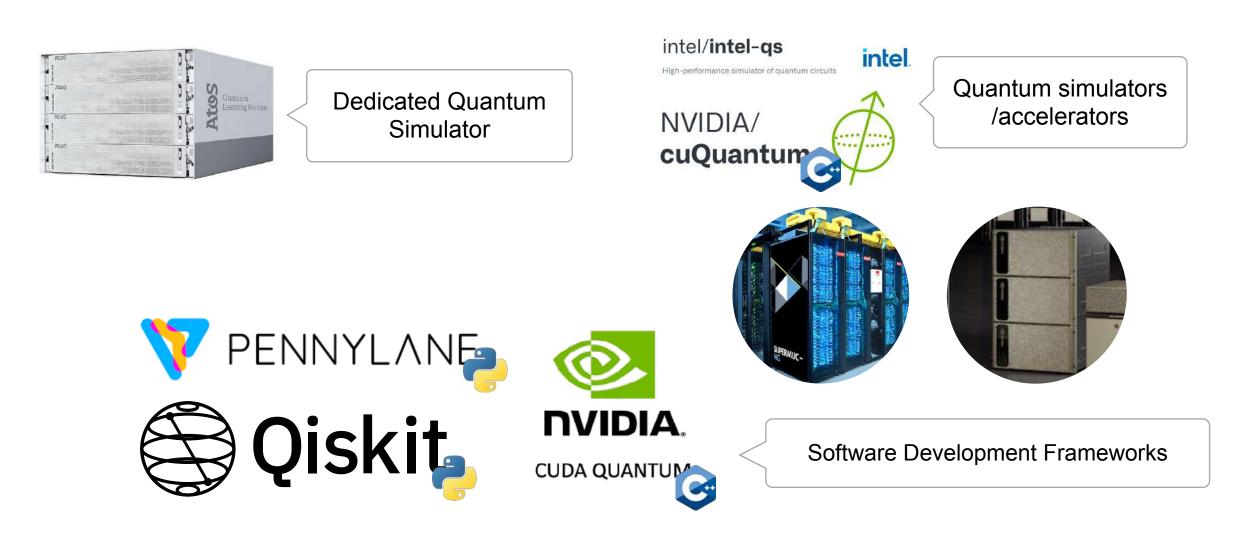
CUDA QUANTUM





Software Development Frameworks



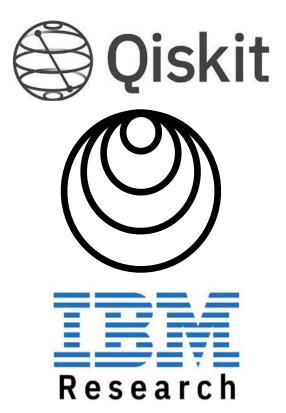


Upscaling QC Simulators on HPC Systems Pennylane and Qiskit





https://github.com/PennyLaneAl/pennylane-lightning-gpu

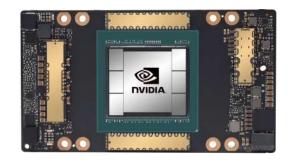


https://github.com/Qiskit/qiskit-aer

Upscaling QC Simulators on HPC Systems Which HW HPC Technologies?







#### NVIDIA DGX A100 80GB

- 64 cores AMD EPYC 7742
- 8 GPU A100 (Ampere architecture)
- Up to 36 qubits in full state vector simulation



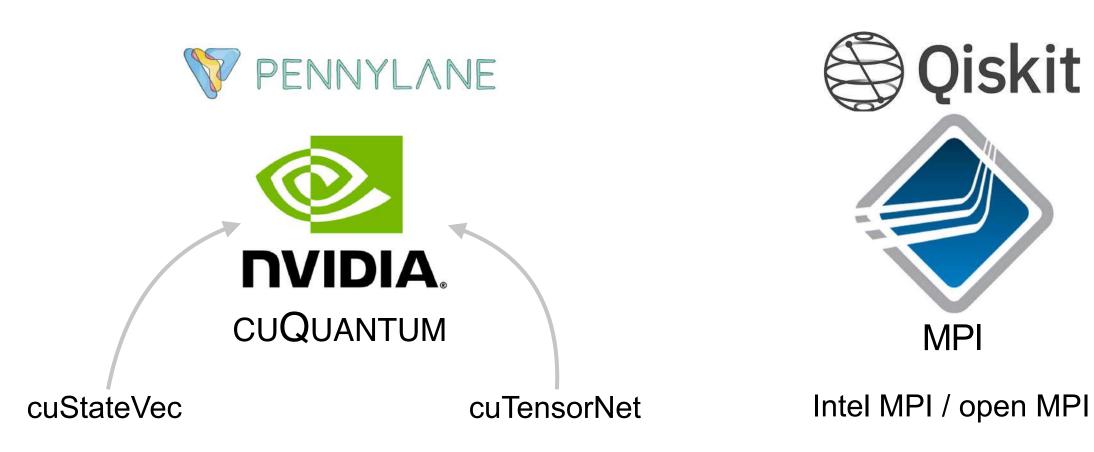


#### Intel Skylake Xeon

- On flagship cluster SuperMUC-NG
- 311,000+ cores
- 26.9 PFlops

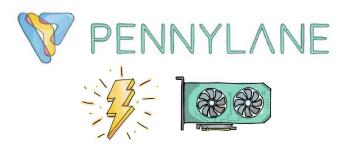
Upscaling QC Simulators on HPC Systems Which SW backend?



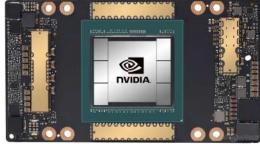


Deployment — Pennylane lightning GPU







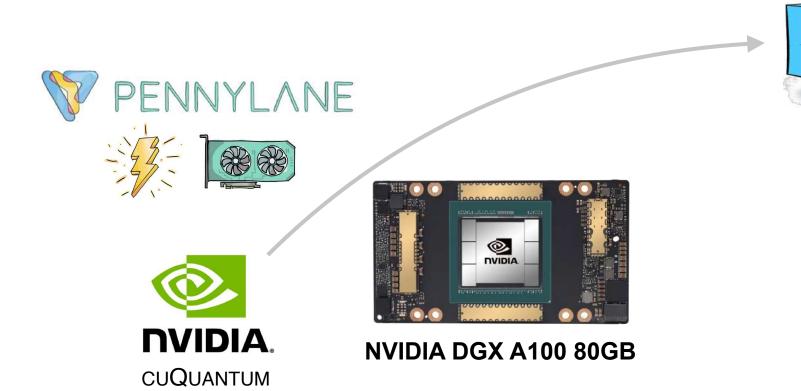


#### NVIDIA DGX A100 80GB

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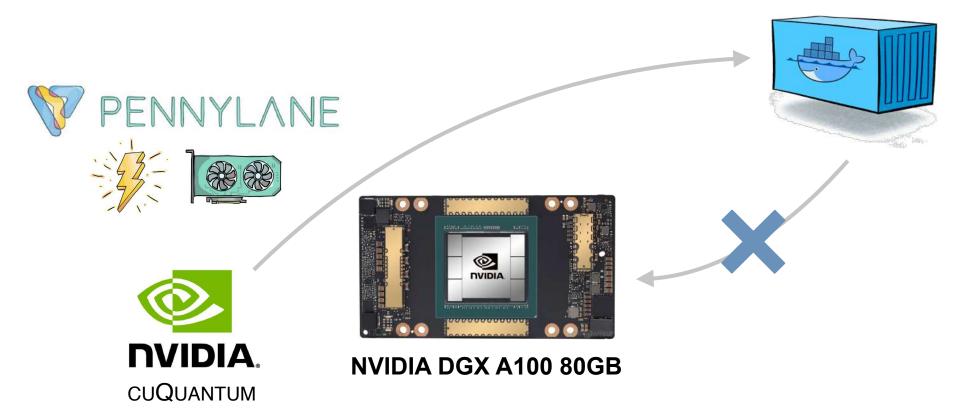
Deployment — Pennylane lightning GPU





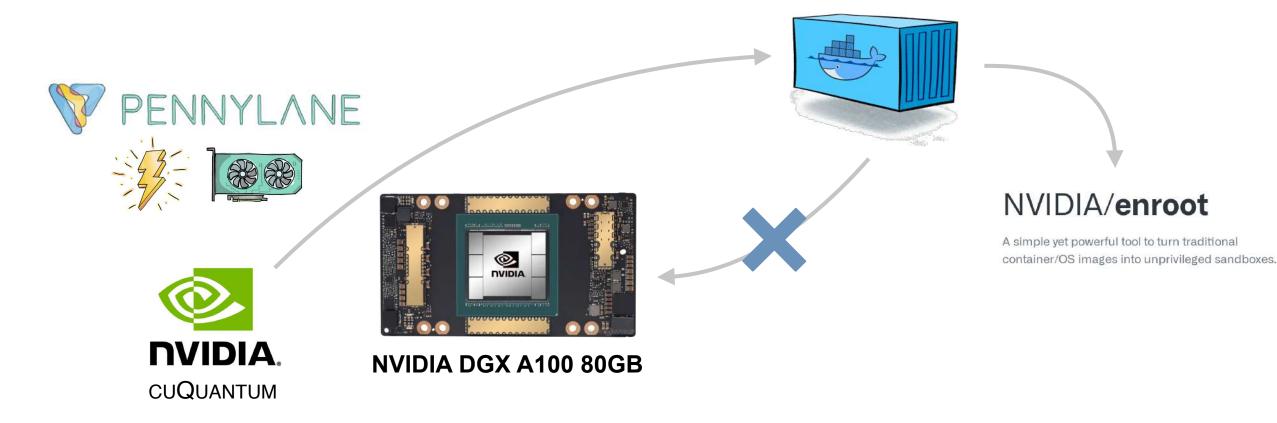
## Deployment — Pennylane lightning GPU





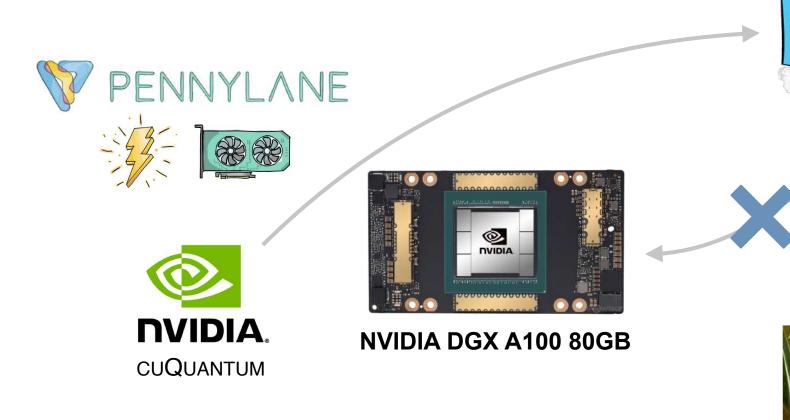
## Deployment — Pennylane lightning GPU





## Deployment — Pennylane lightning GPU

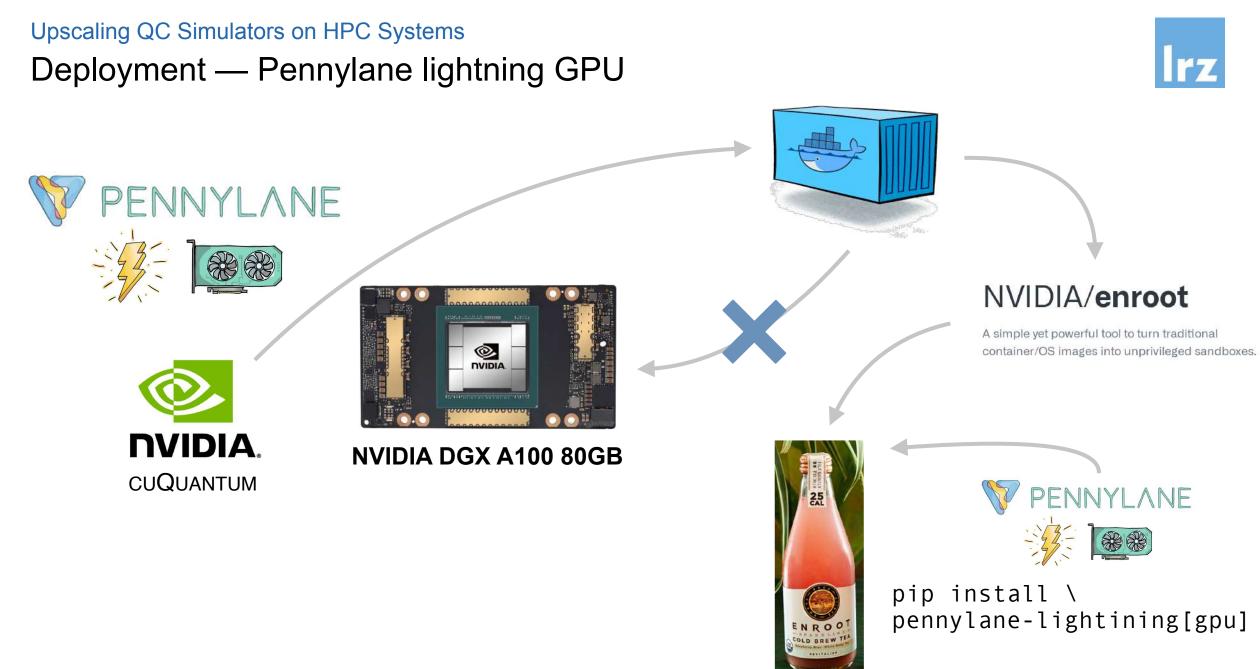


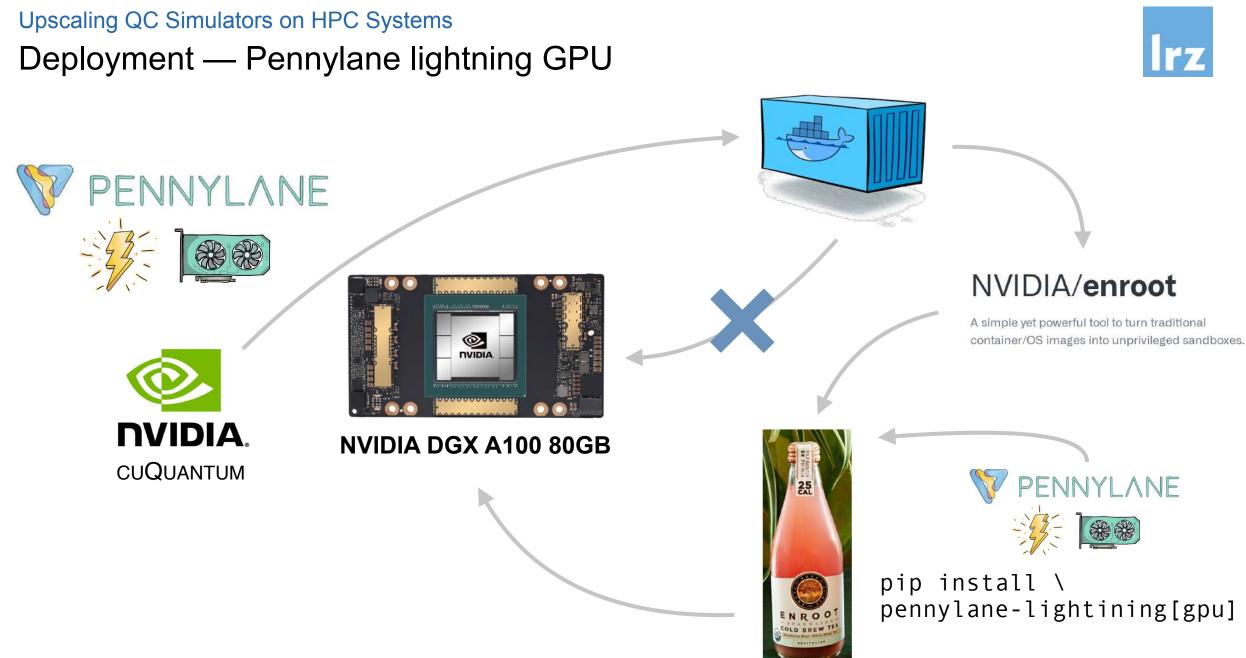




A simple yet powerful tool to turn traditional container/OS images into unprivileged sandboxes.

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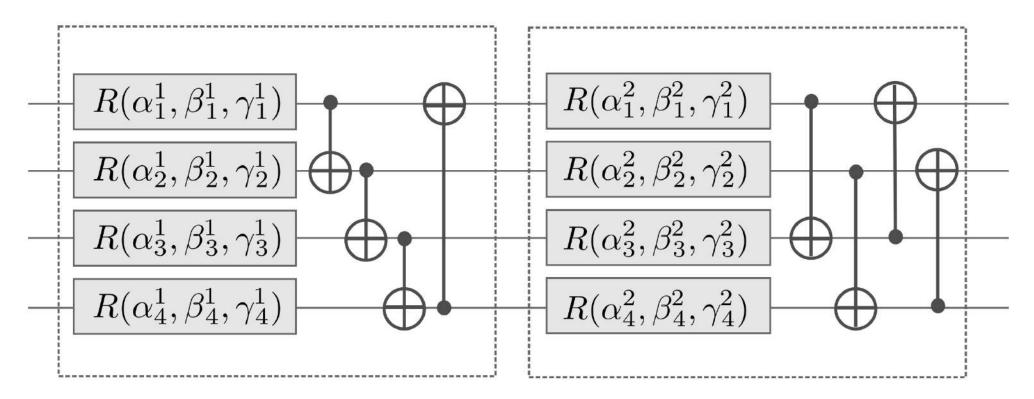




## Upscaling QC Simulators on HPC Systems Pennylane lightning GPU — Simulation



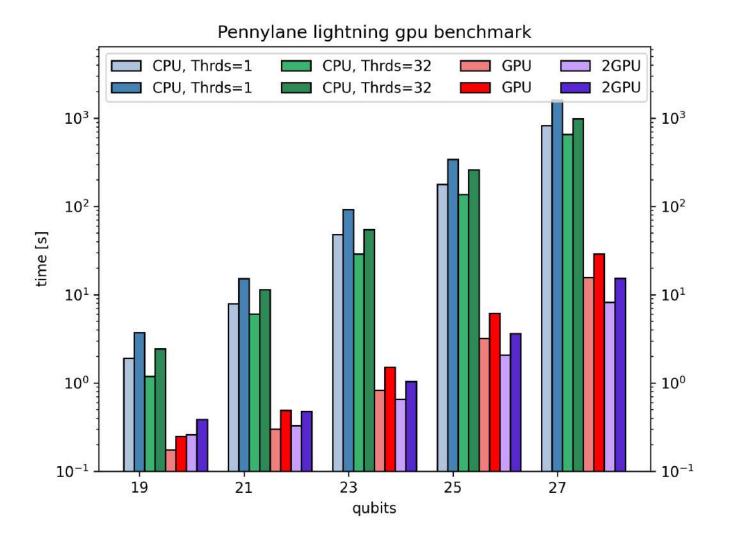
• Strongly Entangling Layers Circuit



• One measure of circuit's Jacobian repeated 50 times, no optimisation

### Upscaling QC Simulators on HPC Systems Pennylane lightning GPU — Results





## Deployment — qiskit-aer and MPI support



pip install qiskit-aer-mpi



#### Intel Skylake Xeon

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## Deployment — qiskit-aer and MPI support



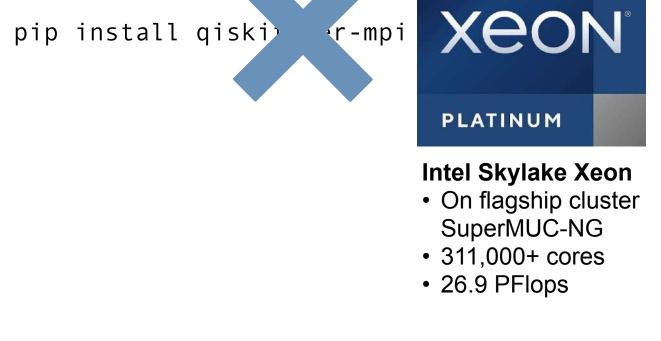


## Deployment — qiskit-aer and MPI support

intel



- MPI support build from source
- Dependencies + build chain
  - openBLAS
  - openMPI
  - gcc 11.2



lrz

Programming Language — C++



- Developed by NVIDIA
- Only a possible backend as simulator; the NVIDIA cuQuantum
- Possible to use real QPU as backends
- Supports QIR (good since goes towards a standard approach <u>https://www.qir-alliance.org/</u>)

lrz

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rz

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- C++ SDK developed by Quantum Brilliace
- Provides access to different simulator backends
- Quantum kernels can be written in CUDA Quantum