

# **Recent Approaches for Efficient Compiling** of Quantum Circuits

# Michele Amoretti<sup>1,2</sup>

1. Department of Engineering and Architecture - University of Parma, Italy 2. Quantum Information Science @ University of Parma, Italy

www.gis.unipr.it

Contact: michele.amoretti@unipr.it

Quantum Computing and HPC



CINECA, 19 December 2019



# Quantum Software @ UniPr

## Team

- Michele Amoretti (Associate Professor)
- Davide Ferrari (PhD student)

## Topics

- high performance computing (classical and quantum)
- quantum compiling
- quantum networking (protocols for distributed monitoring, anonymity, leader election, etc.)

## Source code

https://github.com/gis-unipr









# Molecular Magnetism Group @ UniPr

#### Team

- Stefano Carretta (Full Professor)
- Paolo Santini (Full Professor)
- Elena Garlatti (Research Associate)
- Alessandro Chiesa (Post-doc)
- Roberto De Renzi (Full Professor)



Topics



LETTERS https://doi.org/10.1038/s41567-019-0437-4

#### Quantum hardware simulating four-dimensional inelastic neutron scattering

A. Chiesa<sup>15</sup>, F. Tacchino<sup>2,5</sup>, M. Grossi<sup>2,3</sup>, P. Santini<sup>1</sup>, I. Tavernelli<sup>4</sup>, D. Gerace<sup>2</sup> and S. Carretta<sup>1</sup>



#### Coherent Manipulation of a Molecular Ln-Based Nuclear Qudit **Coupled to an Electron Qubit**

Riaz Hussain,<sup>†</sup> Giuseppe Allodi,<sup>†</sup> Alessandro Chiesa,<sup>†©</sup> Elena Garlatti,<sup>†,‡</sup> Dmitri Mitcov,<sup>¶</sup> Andreas Konstantatos,<sup>¶</sup> Kasper S. Pedersen,<sup>¶,§</sup> Roberto De Renzi,<sup>†</sup> Stergios Piligkos,<sup>¶</sup> and Stefano Carretta\*\*\*\*





## • Francesco Petiziol (Post-Doc) Emilio Macaluso (PhD student) Simone Chicco (PhD student) • Luca Crippa (PhD student / IBM)







Multilevel structure exploited to encode a qubit with embedded Quantum Error Correction.



# Outline

- **Quantum Compilation Problem**
- State of the Art
- Deterministic Pattern-oriented Quantum Compiler (DPQC)
  - Performance Evaluation





# **Quantum Compilation Problem**

## Fast, device-aware implementation of quantum algorithms

A good quantum compiler must translate an input program into the most efficient equivalent of itself, getting the most out of the available hardware

The quantum compilation problem in general is NP-Hard









# **Quantum Compilation Problem**

On noisy intermediate-scale quantum (NISQ) devices:

- **Gate synthesis** = decomposition of an arbitrary unitary operation into a sequence of gates lacksquarefrom a discrete set
- Compliance with the **coupling map**
- Noise awareness

Quality indicators:

- Circuit **depth**
- Gate count
- Fidelity of quantum states

Actions:

- Decide initial mapping
- Exchange qubit states using **SWAP gates**









# **Example: GHZ State**

## The ideal quantum circuit that generates the GHZ state requires only H and CNOT gates



D. M. Greenberger, M. A. Horne, A. Zeilinger, Going Beyond Bell's Theorem, in 'Bell's Theorem, Quantum Theory, and Conceptions of the Universe', M. Kafatos (Ed.), Kluwer, Dordrecht, 69-72 (1989)

![](_page_6_Picture_5.jpeg)

![](_page_7_Picture_0.jpeg)

# Example: GHZ State

#### Ideal vs. compiled 8-qubit GHZ circuit

![](_page_7_Figure_3.jpeg)

(ar)	a · 10\	$I_{L}(\pi/2, 0, -\pi/2)$	•	$11_{10}(0, \pi/2)$	11/0 -47	-1 58)		
(4'0)	<i>q</i> <sub>0</sub> .  0/	$-0_3(\pi/2, 0, -\pi/2)$	Ī	02(0, 1/2)	03(0, -4.7,	-1.50)		
$(qr_1)$	$q_1: 0 angle$	$U_{3}(\pi/2, -\pi/1, -\pi/2)$		$- U_3(\pi/2, \pi/2, -\pi/1)$		π/2) — •	$- \oplus U_3(\pi/2, \pi/2, -\pi$	/2)
$(qr_2)$	$q_2: 0 angle$	U <sub>2</sub> (0, π/1)	φΦ		•		U_3(0, −0.143, −4	U <sub>2</sub> (0, π/2)
(qr <sub>3</sub> )	$q_3: 0 angle$	$U_3(\pi/1, 3.2, 1.63)$						
$(qr_4)$	$q_4: 0 angle$		$  \phi   \phi$		φ			
$(qr_5)$	$q_5: 0 angle$	$U_2(0, \pi/1)$					• • •	
$(qr_6)$	$q_6: 0 angle$	$U_2(0, \pi/1)$	€		₽		-⊕-∳	
(qr <sub>7</sub> )	$q_7: 0 angle$	$U_2(0, \pi/1)$						
(ancilla <sub>0</sub> )	$q_8$ : $ 0\rangle$	)						
ancilla1)	$a_0$ : $ 0\rangle$							
(anemal)	49.10/		- (	V				

![](_page_7_Picture_5.jpeg)

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$U_{3}(\pi/2, -\pi/2, -\pi/2)$		$\begin{array}{c c} & & & & & & & & & & & & & & & & & & &$	$- U_2(0, \pi/1)$
		• $U_2(0, \pi/1)$	
			$U_2(0, \pi/1)$
	U <sub>2</sub> (0, π/1)		

![](_page_8_Picture_0.jpeg)

# Zulehner *et al.*, An Efficient Methodology for Mapping Quantum Circuits to the IBM QX Architectures, IEEE TCAD vol.38 no.7, 2019

- Partition circuit into layers
- For each layer find a CNOT compliant mapping
   *m!/(m-n)!* possible mappings with *m* physical qu
  - *m!/(m-n)!* possible mappings with *m* physical qubits and *n* logical qubits
     A\* search algorithm to find less expensive swap sequence
  - $\circ~$  minimize additional operations to switch between subsequent mappings

![](_page_8_Figure_7.jpeg)

![](_page_8_Picture_8.jpeg)

![](_page_9_Picture_0.jpeg)

## Jandura, 2018 (LookaheadSwap in Qiskit)

Mapping = logical qubits ↔ physical qubits

Given a mapping and a list of upcoming CNOTs, repeat 4 times:

- 1. Find 4 most promising swaps and calculate the new mapping for each of them
- 2. For each mapping count how many CNOTs can be executed and remove them from the list

From the 4<sup>4</sup>=256 final mappings, choose the one that allowed for the most CNOTs to be executed and execute the fist swap on the path to this mapping

Then start the whole algorithm again, until the circuit is completed

![](_page_9_Picture_9.jpeg)

![](_page_9_Figure_10.jpeg)

![](_page_10_Picture_0.jpeg)

# *Li et al., Tackling the Qubit Mapping Problem for NISQ-Era Quantum Devices,* ASPLOS '19, 2019

SWAP-based BidiREctional heuristic search (SABRE)

## • Preprocessing

- Compute distance matrix *D* over the coupling map
- Generate DAG from circuit to represent two-qubit gates dependencies
- Initialize the front layer F as the set of two-qubit gates without unexecuted predecessors
- Generate random initial mapping
- Iterate over front layer F, stop when F is empty
  - $\circ\,$  Remove executable gates from F and add their successor to F
  - For those gates in *F* that cannot be executed, select best SWAP sequence using an heuristic cost function based on distance matrix *D*

![](_page_10_Picture_12.jpeg)

![](_page_10_Figure_13.jpeg)

![](_page_11_Picture_0.jpeg)

## qiskit.transpiler

qc\_comp = transpile(qc, backend=backend, coupling\_map=coupling\_map, pass\_manager=pm)

The PassManager schedules the **passes**:

- Layout selection
- Unrolling
- Swap Mapping
  - BasicSwap
  - StochasticSwap
  - LookaheadSwap
- Gate optimizations
- etc.

![](_page_11_Picture_13.jpeg)

![](_page_11_Picture_15.jpeg)

![](_page_12_Picture_0.jpeg)

# **Deterministic Pattern-oriented Quantum Compiler (DPQC)**

The DPQC software is the implementation of **efficient and deterministic strategies** for the compilation of quantum circuits characterized by peculiar sequences (patterns) of two-qubit operators.

By compilation we mean the transformation of abstract quantum circuits into circuits suited to the characteristics of the available hardware device, preserving their correctness and trying to maximize their efficiency.

Among the possible characteristics of a quantum computer, the DPQC software considers the coupling map and tries to **minimize circuit depth and gate count**.

## SIAE filing n.2019002328, 11/9/2019

![](_page_12_Picture_6.jpeg)

![](_page_13_Picture_0.jpeg)

# **CNOT Sequences Without Gaps**

![](_page_13_Figure_2.jpeg)

- Algorithm *chain\_layout()* analyzes the coupling map and orders the qubits in a *chain*
- Every logical qubit  $\mathbf{q}_{i}$  is associated with the *i-th* physical qubit in the chain
- Initial mapping satisfies all CNOTs
- In general, the problem of finding a path that visits every node only once is NP-complete
- *chain layout()* exploits the qubit indexing and the graph structure to find a path with a deterministic and efficient approach
- Computational complexity **O(m)**, where *m* is the number of physical qubits

![](_page_13_Picture_9.jpeg)

![](_page_13_Figure_12.jpeg)

![](_page_14_Picture_0.jpeg)

# **CNOT Sequences With Gaps**

- Initial mapping is not enough
- Algorithm *path()* moves the qubits  $q_1$  and  $q_2$  involved in a remote CNOT close to each other:
  - $\circ$  iteratively computes the SWAP path between  $q_1$  and  $q_2$  neighbors
  - $\circ$  evaluates the distance between  $q_x$  and  $q_y$  as |x y| with x and y being their physical qubit indices in the coupling map
  - uses a cost function to choose which qubit to add to the path

$$f(x,y) = |x-y| * SV$$

- $\circ$  updates  $q_1$  and  $q_2$  mapping after every iteration
- Computational complexity **O(m)**

![](_page_14_Picture_10.jpeg)

![](_page_14_Figure_11.jpeg)

 $VAP\_DEPTH$ 

![](_page_15_Picture_0.jpeg)

![](_page_15_Figure_1.jpeg)

Repeated sequences of inverted CNOT cascades

- CNOT cascades are equivalent to CNOT sequences
- Algorithm *analyze\_circuit()* detects CNOT cascades and turns them into CNOT sequences
- Computational complexity O(gn) with g being the number of gates in the circuit

![](_page_15_Picture_6.jpeg)

![](_page_15_Figure_7.jpeg)

![](_page_16_Picture_0.jpeg)

# Inverted CNOT Cascades

- CNOT gates can be inverted by applying H gates before and after involved qubits
- analyze\_circuit() also detects inverted CNOT cascades and turns them into CNOT cascades
- Algorithm gate\_cancellation() deletes double CNOT and double H gates
- Computational complexity **O(dn<sup>2</sup>)**

![](_page_16_Figure_6.jpeg)

![](_page_16_Picture_7.jpeg)

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![](_page_16_Figure_9.jpeg)

![](_page_17_Picture_0.jpeg)

# **GHZ Results**

![](_page_17_Figure_2.jpeg)

![](_page_17_Picture_3.jpeg)

![](_page_18_Picture_0.jpeg)

# **Quantum Chemistry Circuits**

Used to compute the ground state wave function of simple molecular systems such as:

- Hydrogen H<sub>2</sub> with 4 qubits
- Lithium Hydride LiH with 12 qubits
- Water H<sub>2</sub>O with 14 qubits

![](_page_18_Picture_6.jpeg)

Ivano Tavernelli Theoretical Quantum Computing @ IBM Research - Zurich

European Quantum Technology Conference (EQTC19) The first international conference of the QT Flagship enoble, France, February 18th - 22nd (2019)

![](_page_18_Picture_9.jpeg)

#### **Efficient Quantum Compiling for Quantum Chemistry** Simulation on IBM Q

D. Ferrari<sup>\*</sup>, I. Tavernelli<sup>†</sup>, M. Amoretti<sup>@\*</sup>

<sup>@</sup>michele.amoretti@unipr.it, \*Department of Engineering and Architecture - University of Parma, Italy, †IBM Zurich Research Laboratory

![](_page_19_Picture_0.jpeg)

# UCCSD

![](_page_19_Figure_2.jpeg)

# RyRz

![](_page_19_Figure_4.jpeg)

![](_page_19_Picture_5.jpeg)

![](_page_20_Picture_0.jpeg)

# **UCCSD Results**

	Circuit Depth			
Circuit Name	Input Circuit	DPQC	Basic	Stochastic
H2_UCCSD	82	64	64	64
$LiH_UCCSD$	8845	6717	12201	8026
$H2O\_UCCSD$	15388	13413	12797	14789
$Random 20\_UCCSD$	125638	132865	204927	173672

	Compilation Time $(s)$				
Circuit Name	DPQC	Basic	Stochastic		
$H2\_UCCSD$	1	1	1		
LiH_UCCSD	221	247	314		
H2O_UCCSD	575	393	756		
$Random 20\_UCCSD$	18448	11230	20127		

![](_page_20_Picture_4.jpeg)

#### Backend: QX20 Tokyo

![](_page_21_Picture_0.jpeg)

# **RyRz Results**

	Circuit Depth			
Circuit Name	Input Circuit	DPQC	Basic	Stochastic
H2_RyRz	73	40	45	45
LiH_RyRz	233	160	898	615
H2O_RyRz	273	190	1258	841
Random20_RyRz	393	279	3552	1629

	Compilation Time $(s)$				
Circuit Name	DPQC	Basic	Stochastic		
H2_RyRz	1	1	1		
LiH_RyRz	4	20	24		
H2O_RyRz	5	36	28		
Random20_RyRz	8	93	65		

![](_page_21_Picture_4.jpeg)

Backend: QX20 Tokyo

![](_page_22_Picture_0.jpeg)

# **Other Benchmarks**

	Circuit Depth			
Circuit Name	Input Circuit	DPQC	Basic	Stochastic
9symml_195	19235	35406	38489	40002
$clip_206$	17879	34715	33877	38726
$co14_{-}215$	8570	18294	18551	19458
$dist_223$	19694	36144	36488	42289
$life_238$	12511	23319	24974	25990
$max46_240$	14257	27466	27550	29650
$sao2_257$	19563	39268	40202	42988
$sqn_258$	5458	9999	10599	11271
$sym10_262$	35575	67362	64885	75168

## Backend: QX20 Tokyo

![](_page_22_Picture_4.jpeg)

	Compilation Time $(s)$			
Circuit Name	DPQC	Basic	Stochastic	
9symml_195	612	626	802	
$clip_206$	759	570	817	
$co14_{-}215$	377	316	450	
$dist_223$	783	626	951	
$life_238$	358	397	538	
$max46_240$	431	429	660	
$sao2_257$	871	691	1036	
$sqn_258$	148	170	239	
$sym10_262$	1543	1119	1987	

![](_page_23_Picture_0.jpeg)

# **Future Work**

- Testing the proposed approach with the **new coupling maps**
- Using extra qubits
- Studying other circuit patterns

![](_page_23_Figure_5.jpeg)

![](_page_23_Picture_6.jpeg)

![](_page_23_Figure_8.jpeg)

![](_page_24_Picture_0.jpeg)

# Future Work

While effective, circuit depth and gate count are only pseudo-objectives. In reality, what matters is the **fidelity** of a computation when run on actual quantum hardware.

NISQ compilers excel when more information is made available to them from the device.

IBM Q device properties are shared openly and can be benchmarked, resulting in a bunch of compiler innovations.

P. Murali et al., Noise-Adaptive Compiler Mappings for Noisy Intermediate-Scale Quantum Computers, arXiv:1901.11054, 2019

S. Nishio *et al.*, Extracting Success from IBM's 20-Qubit Machines Using Error-Aware Compilation, arXiv:1903.10963, 2019

![](_page_24_Picture_7.jpeg)

![](_page_25_Picture_0.jpeg)

# Thank You!

# Questions?

![](_page_25_Picture_3.jpeg)

![](_page_26_Picture_0.jpeg)

# Sequence Offset

![](_page_26_Figure_2.jpeg)

8 qubits chain with *offset*=12

![](_page_26_Picture_4.jpeg)

- 0 ≤ *offset* < (*m*-*n*)
- Used to map the qubits in the circuit to the qubits in the chain in the interval {0,...,n + offset} instead of {0,...,n}

![](_page_27_Picture_0.jpeg)

# Impact of Sequence Offset

## **UCCSD LiH**

![](_page_27_Figure_3.jpeg)

![](_page_27_Picture_4.jpeg)